

# SGM3157 4.5Ω Low Voltage SPDT Analog Switch in 6-pin SC70

#### **GENERAL DESCRIPTION**

The SGM3157 is a single, bidirectional, single-pole/double-throw (SPDT) CMOS analog switch that is designed to operate from a single +1.8V to +5.5V supply. It features high-bandwidth (300MHz) and low on- resistance (4.5 $\Omega$  TYP), Targeted applications for audio switching.

SGM3157 features guaranteed on-resistance matching (0.3 $\Omega$  MAX) between switches and guaranteed on-resistance flatness over the signal range (2.3 $\Omega$  TYP). This ensures excellent linearity and low distortion when switching audio signals.

SGM3157 is available in a SC70-6 package.

## **FEATURES**

- Voltage Operation: 1.8V to 5.5V
  On-Resistance: 4.5Ω (TYP) at 5.0V
- Fast Switching Times
  - t<sub>ON</sub> 20ns
  - toff 15ns
- High Bandwidth: 300MHz
- High Off-Isolation: -51dB at 10MHz
- Rail-to-Rail Operation
- TTL/CMOS Compatible
- Break-Before-Make Switching
- Extended Industrial Temperature Range:
   -40°C to 85°C
- Lead (Pb) Free SC70-6 Package

#### **APPLICATIONS**

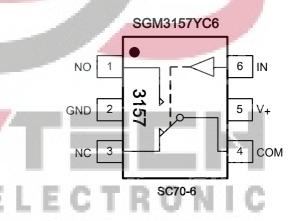
Portable Instrumentation
Battery-Operated Equipment
Computer Peripherals
Cell Phones
PDAs
MP3s

#### **FUNCTION TABLE**

LOGIC	NO	NC
0	OFF	ON
1	ON	OFF

Switches Shown for Logic "0" Input

## PIN CONFIGURATION (TOP VIEW)



#### ORDERING INFORMATION

MODEL	PIN- PACKAGE	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKAGE OPTION	
SGM3157	SC70-6	-40°C to +85°C	SGM3157YC6/TR	3157	Tape and Reel, 3000	

Note: SC70-6 package is same as SOT-363 package.

### ABSOLUTE MAXIMUM RATINGS

V <sub>+</sub> , IN to GND0.3V to 6V	Junction Temperature150°C
Analog, Digital voltage range $^{(1)}$ 0.3V to $(V_+)$ + 0.3V	Storage Temperature65°C to +150°C
Continuous Current NO, NC, or COM±50mA	Lead Temperature (soldering, 10s)
Peak Current NO, NC, or COM±80mA	ESD (HBM)
Operating Temperature Range40°C to +85°C	ESD (MM)

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

(1) Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

#### CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

#### PIN DESCRIPTION

NAME	PIN	FUNCTION
NO	1	Normally-open terminal
GND	2	Ground
NC	3	Normally-closed terminal
COM	4	Common terminal
V <sub>+</sub>	5	Power supply
IN	6	Digital control pin to connect the COM terminal to the NO or NC terminals

Note: NO, NC and COM terminals may be an input or output.



#### **ELECTRICAL CHARACTERISTICS**

 $(V_+ = +4.5V \text{ to } +5.5V, V_{IH} = +2.0V, V_{IL} = +0.8V, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ Typical values are at } V_+ = 5.0V, T_A = +25^{\circ}\text{C}, \text{ unless otherwise noted.})$ 

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
ANALOG SWITCH				ı			
Analog Signal Range	$V_{NO}, V_{NC}, V_{COM}$		-40°C to +85°C	0		V+	V
On Basistanas	D	$V_{+} = 4.5V$ , $V_{NO}$ or $V_{NC} = 3.5V$ ,	+25°C		4.5	8	Ω
On-Resistance	R <sub>ON</sub>	I <sub>COM</sub> = -10mA, Test Circuit 1	-40°C to +85°C	1		8.5	Ω
On-Resistance Match	AD	$V_{+} = 4.5V$ , $V_{NO}$ or $V_{NC} = 3.5V$ ,	+25°C		0.15	0.3	Ω
Between Channels	ΔR <sub>ON</sub>	I <sub>COM</sub> = -10mA, Test Circuit 1	-40°C to +85°C			0.4	Ω
On-Resistance Flatness	R <sub>FLAT(ON)</sub>	$V_{+} = 4.5V$ , $V_{NO}$ or $V_{NC} = 1.0V$ , $2.0V$ , $3.5V$ ,	+25°C		2.3	3.3	Ω
On-resistance matrices	TYFLAT(ON)	I <sub>COM</sub> = -10mA, Test Circuit 1	-40°C to +85°C			3.7	Ω
Source OFF Leakage Current	Inc(off), Ino(off)	$V_{+}$ = 5.5V, $V_{NO}$ or $V_{NC}$ = 1.0V, 4.5V, $V_{COM}$ = 4.5V, 1.0V	-40°C to +85°C			1	μΑ
Channel ON Leakage Current	I <sub>NC(ON)</sub> , I <sub>NO(ON)</sub> , I <sub>COM(ON)</sub>	$V_{+}$ = 5.5V, $V_{COM}$ = 1.0V, 4.5V, $V_{NO}$ or $V_{NC}$ = 1.0V, 4.5V, or floating	-40°C to +85°C			1	μΑ
DIGITAL INPUTS							
Input High Voltage	V <sub>INH</sub>		-40°C to +85°C	1.5			V
Input Low Voltage	V <sub>INL</sub>		-40°C to +85°C			0.6	<b>V</b>
Input Leakage Current	I <sub>IN</sub>	$V_{+} = +5.5V$ , $V_{IN} = 0V$ or $5.5V$	-40°C to +85°C		10	1	μΑ
DYNAMIC CHARACTERIS	STICS						
Turn-On Time	ton	$V_{NO}$ or $V_{NC}$ = 3.0V, $V_{IH}$ = 1.5V, $V_{IL}$ = 0V, $R_L$ = 300 $\Omega$ , $C_L$ = 35pF, Test Circuit 2	+25°C		20		ns
Turn-Off Time	toff	$V_{NO}$ or $V_{NC}$ = 3.0V, $V_{IH}$ = 1.5V, $V_{IL}$ = 0V, $R_L$ = 300 $\Omega$ , $C_L$ = 35pF, Test Circuit 2	+25°C	-48	15		ns
Break-Before-Make Time Delay	t <sub>D</sub>	$V_{NO1}$ or $V_{NC1}$ = $V_{NO2}$ or $V_{NC2}$ = 3V, $R_L$ = 300 $\Omega$ , $C_L$ = 35pF, Test Circuit 3	+25°C		5		ns
Skew	t <sub>SKEW</sub>	RS = $39\Omega$ , C <sub>L</sub> = $50$ pF, Test Circuit 4	+25°C		5		ns
Off Isolation	O <sub>ISO</sub>	$R_L = 50\Omega$ , $C_L = 5pF$ , $f = 10MHz$	+25°C		-51		dB
On isolation	Olso	Signal = 0dBm, Test Circuit 5 f = 1MHz	+25°C		-72		dB
-3dB Bandwidth	BW	Signal = $0dBm$ , $R_L = 50\Omega$ , $C_L = 5pF$ , Test Circuit 6	+25°C		300		MHz
Source OFF Capacitance	C <sub>NC(OFF)</sub> , C <sub>NO(OFF)</sub>	f = 1MHz	+25°C	N	5.5		pF
Channel ON Capacitance	$C_{NC(ON)}, C_{NO(ON)}, C_{COM(ON)}$	f = 1MHz	+25°C		15.5		pF
POWER REQUIREMENTS							
Power Supply Range	V <sub>+</sub>		-40°C to +85°C	1.8		5.5	V
Power Supply Current	I+	V <sub>+</sub> = +5.5V, V <sub>IN</sub> = 0V or V <sub>+</sub>	-40°C to +85°C			5	μΑ

Specifications subject to change without notice.



## **ELECTRICAL CHARACTERISTICS**

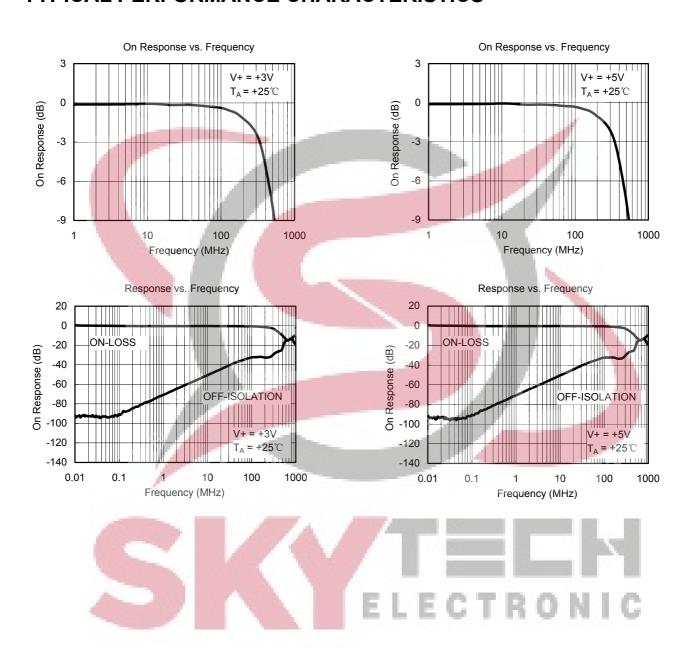
 $(V_{+} = +2.7V \text{ to } +3.6V, V_{IH} = +1.4V, V_{IL} = +0.5V, T_{A} = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ Typical values are at } V_{+} = 3.0V, T_{A} = +25^{\circ}\text{C}, \text{ unless otherwise noted.})$ 

noted.) PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
ANALOG SWITCH			<u> </u>		1		
Analog Signal Range	V <sub>NO</sub> , V <sub>NC</sub> , V <sub>COM</sub>		-40°C to +85°C	0		V <sub>+</sub>	V
		$V_{+} = 2.7V_{,}V_{NO} \text{ or } V_{NC} = 1.5V_{,}$	+25°C		7	10	Ω
On-Resistance	R <sub>ON</sub>	I <sub>COM</sub> = -10mA, Test Circuit 1	-40°C to +85°C			10.5	Ω
On-Resistance Match	AD	$V_{+} = 2.7V$ , $V_{NO}$ or $V_{NC} = 1.5V$ ,	+25°C		0.15	0.3	Ω
Between Channels	$\Delta R_{ON}$	I <sub>COM</sub> = -10mA, Test Circuit 1	-40°C to +85°C			0.4	Ω
On-Resistance Flatness	Dr. 1770U	$V_{+} = 2.7V$ , $V_{NO}$ or $V_{NC} = 1.0V$ , $1.5V$ , $2.0V$ ,	+25°C		3	4	Ω
On-Resistance Flatness	R <sub>FLAT(ON)</sub>	I <sub>COM</sub> = -10mA, Test Circuit 1	-40°C to +85°C			4.3	Ω
Source OFF Leakage Current	Inc(off), Ino(off)	$V_{+}$ = 3.6V, $V_{NO}$ or $V_{NC}$ = 0.3V, 3.3V, $V_{COM}$ = 3.3V, 0.3V,	-40°C to +85°C			1	μΑ
Channel ON Leakage Current	I <sub>NC(ON)</sub> , I <sub>NO(ON)</sub> , I <sub>COM(ON)</sub>	$V_{+} = 3.6V$ , $V_{COM} = 0.3V$ , 3.3V, $V_{NO}$ or $V_{NC} = 0.3V$ , 3.3V, or floating	-40°C to +85°C			1	μΑ
DIGITAL INPUTS			100				
Input High Voltage	V <sub>INH</sub>		-40°C to +85°C	1			V
Input Low Voltage	V <sub>INL</sub>		-40°C to +85°C	- 1	L.	0.5	V
Input Leakage Current	I <sub>IN</sub>	V <sub>+</sub> = +5.5V, V <sub>IN</sub> = 0V or 3.6V	-40°C to +85°C	1.1.		1	μA
DYNAMIC CHARACTERI	STICS						
Turn-On Time	t <sub>on</sub>	$V_{NO}$ or $V_{NC}$ = 1.5V, $V_{IH}$ = 1.5V, $V_{IL}$ = 0V, $R_L$ = 300 $\Omega$ , $C_L$ = 35pF, Test Circuit 2	+25°C		30		ns
Turn-Off Time	toff	$V_{NO}$ or $V_{NC}$ = 1.5V, $V_{IH}$ = 1.5V, $V_{IL}$ = 0V, $R_L$ = 300 $\Omega$ , $C_L$ = 35pF, Test Circuit 2	+25°C		25		ns
Break-Before-Make Time Delay	t <sub>D</sub>	$V_{NO1}$ or $V_{NC1}$ = $V_{NO2}$ or $V_{NC2}$ = 3V, R <sub>L</sub> = 300 $\Omega$ , C <sub>L</sub> = 35pF, Test Circuit 3	+25°C		8		ns
Skew	t <sub>SKEW</sub>	$R_S = 39\Omega$ , $C_L = 50pF$ , Test Circuit 4	+25°C		2		ns
Off loolation	0	$R_L = 50\Omega, C_L = 5pF,$	+25°C		-51		dB
Off Isolation	O <sub>ISO</sub>	Signal = 0dBm, Test Circuit 5 f = 1MHz	+25°C		-72		dB
–3dB Bandwidth	BW	Signal = 0dBm, $R_L$ = 50 $\Omega$ , $C_L$ = 5pF, Test Circuit 6	+25°C		300		MHz
Source OFF Capacitance	C <sub>NC(OFF)</sub> , C <sub>NO(OFF)</sub>	f = 1MHz	+25°C	ul I	5.5		pF
Channel ON Capacitance	C <sub>NC(ON)</sub> , C <sub>NO(ON)</sub> , C <sub>COM(ON)</sub>	f = 1MHz	+25°C		15.5		pF
POWER REQUIREMENT	s						
Power Supply Range	V <sub>+</sub>		-40°C to +85°C	1.8		5.5	V
Power Supply Current	l+	$V_{+} = +5.5V$ , $V_{IN} = 0V$ or $V_{+}$	-40°C to +85°C			5	μA

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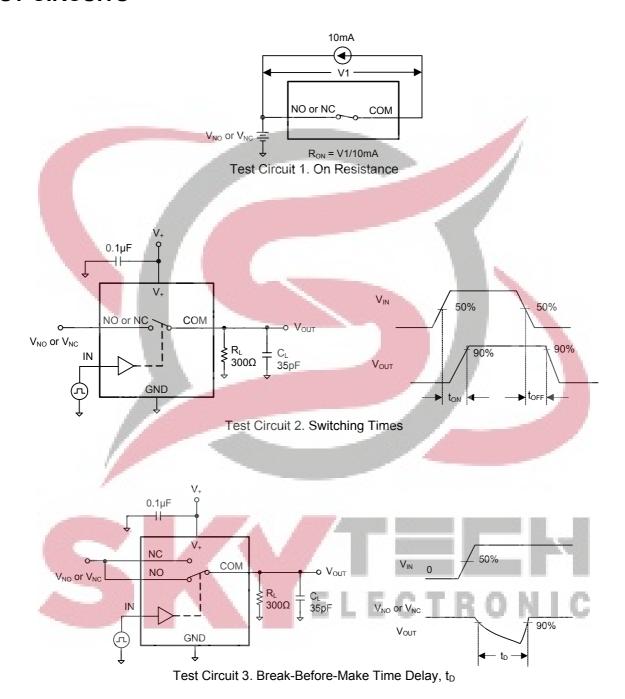


## TYPICAL PERFORMANCE CHARACTERISTICS

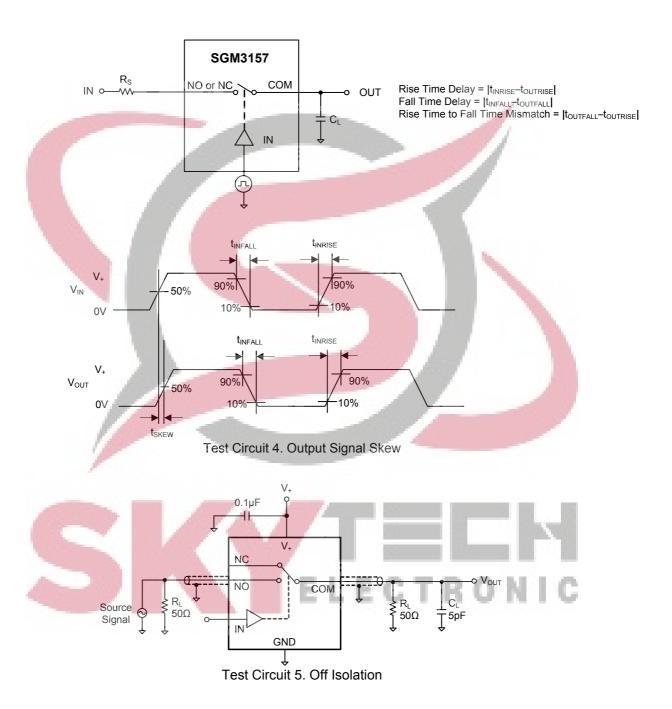




## **TEST CIRCUITS**

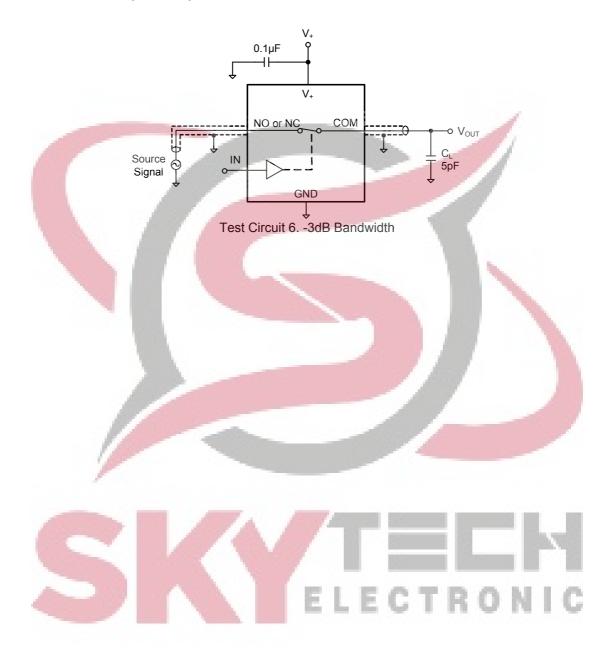


## **TEST CIRCUITS (Cont.)**





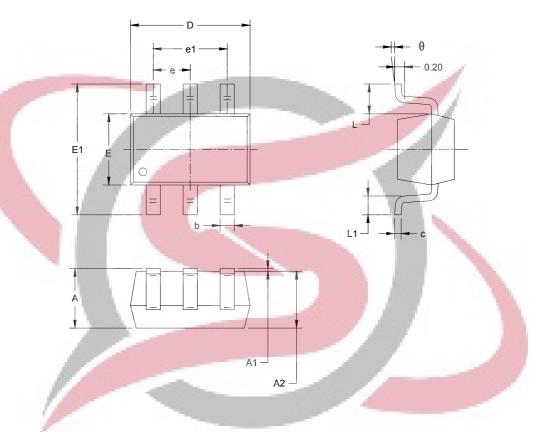
## **TEST CIRCUITS (Cont.)**





## PACKAGE OUTLINE DIMENSIONS

#### SC70-6 / SOT-363



Symbol		nsions imeters	Dimensions In Inches		
Day 1911	MIN	MAX	MIN	MAX	
A	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.150	0.350	0.006	0.014	
С	0.080	0.150	0.003	0.006	
D	2.000	2.200	0.079	0.087	
E	1.150	1.350	0.045	0.053	
E1	2.150	2.450	0.085	0.096	
е	0.65	TYP	0.026 TYP		
e1	1.300 BSC 0.051 BSC			BSC	
L	0.525	REF	0.021 REF		
L1	L1 0.260		0.010	0.018	
θ	0°	8°	0°	8°	





### 04/2010 REV. B. 3

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